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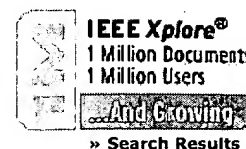
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-	40510	unfold\$3	USPAT; US-PGPUB	2004/10/12 13:28
-	852	unfold\$3 and harness	USPAT; US-PGPUB	2004/10/12 13:28
-	265	(unfold\$3 and harness) and connector	USPAT; US-PGPUB	2004/10/12 13:28
-	51944	((three adj dimension\$3) and (two adj dimension\$3))	USPAT; US-PGPUB	2004/10/12 13:28
-	791	harness and ((three adj dimension\$3) and (two adj dimension\$3))	USPAT; US-PGPUB	2004/10/12 13:28
-	401	(harness and ((three adj dimension\$3) and (two adj dimension\$3))) and model	USPAT; US-PGPUB	2004/10/12 13:28
-	304	((harness and ((three adj dimension\$3) and (two adj dimension\$3))) and model) and parameter	USPAT; US-PGPUB	2004/10/12 13:28
-	197	((((harness and ((three adj dimension\$3) and (two adj dimension\$3))) and model) and parameter) and angle	USPAT; US-PGPUB	2004/10/12 13:28
-	159	(((((harness and ((three adj dimension\$3) and (two adj dimension\$3))) and model) and parameter) and angle) and length	USPAT; US-PGPUB	2004/10/12 13:28
-	17	(harness and (connector adj fitting\$3)) and parameter	USPAT; US-PGPUB	2004/10/12 13:29
-	1	6330746.URPN.	USPAT	2004/10/12 13:29
-	290	703/1.ccor.	USPAT; US-PGPUB	2004/10/12 13:29
-	38	("1760538" "3258039" "3633096" "3836415" "3842496" "3859724" "3861015" "3913202" "3930307" "3946768" "4030029" "4114014" "4202722" "4486058" "4639819" "4711025" "4724612" "4835858" "4965929" "5064384" "5153839" "5205329" "5535511" "5535788" "5543581" "5610447" "5610454" "5614042" "5680330" "5807450" "5829129" "5888324" "5894660" "5911450" "5940962" "5987743" "6101695" "6169934").PN.	USPAT; US-PGPUB	2004/10/12 13:29
-	5	5138698.URPN.	USPAT	2004/10/12 13:29
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-	53	29/872.ccor.	USPAT; US-PGPUB	2004/10/12 13:29
-	34	((unfold\$3 and harness) and connector) and (two adj dimension\$3)	USPAT; US-PGPUB	2004/10/12 13:29
-	280	345/427.ccor.	USPAT; US-PGPUB	2004/10/12 13:29
-	77	(((((harness and ((three adj dimension\$3) and (two adj dimension\$3))) and model) and parameter) and angle) and length) and @ad<="20001019"	USPAT; US-PGPUB	2004/10/12 13:29
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-	922	345/419.ccor.	USPAT; US-PGPUB	2004/10/12 13:29
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Rawal, S.P.; Barnett, D.M.; Martin, D.E.;

Advanced Packaging, IEEE Transactions on [see also Components, Packaging and Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions on], Volume: 22, Issue: 3, Aug. 1999
Pages:379 - 383

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2 The roles of FPGAs in reprogrammable systems

Hauck, S.;

Proceedings of the IEEE, Volume: 86, Issue: 4, April 1998
Pages:615 - 638

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3 The Gas-Tightness of Separable Base Metal Electric Contacts

Tripp, J.; Garte, S.;

Components, Hybrids, and Manufacturing Technology, IEEE Transactions on [see also IEEE Trans. on Components, Packaging, and Manufacturing Technology, Part A, B, C], Volume: 4, Issue: 1, Mar 1981

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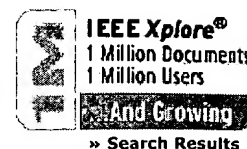
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1 Bayesian surrogates for integrating numerical, analytical, and experimental data: application to inverse heat transfer in wearable computers

Leoni, N.; Amon, C.H.;

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 Pages:23 - 32

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2 Emerging simulation approaches for micromachined devices

Mukherjee, T.; Fedder, G.K.; Ramaswamy, D.; White, J.;

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3 An industrial view of electronic design automation

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4 Harnessing natural textures for multivariate visualization

Interrante, V.;

 Computer Graphics and Applications, IEEE, Volume: 20, Issue: 6, Nov.-Dec. 2000
 Pages:6 - 11

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5 Biomolecular computing and programming

Garzon, M.H.; Deaton, R.J.;

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6 Promises and challenges of evolvable hardware

Yao, X.; Higuchi, T.;

Systems, Man and Cybernetics, Part C, IEEE Transactions on , Volume: 29 , Issue: 1 , Feb. 1999

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7 Codes for digital recorders

Schouhamer Immink, K.E.; Siegel, P.H.; Wolf, J.K.;

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8 Statistical response of EM-driven cables inside an overmoded enclosure

Holland, R.; St. John, R.H.;

Electromagnetic Compatibility, IEEE Transactions on , Volume: 40 , Issue: 4 , Nov. 1998

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9 Humanistic computing: "WearComp" as a new framework and application for intelligent signal processing

Mann, S.;

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10 Local dynamic modeling with self-organizing maps and applications to nonlinear system identification and control

Principe, J.C.; Ludong Wang; Motter, M.A.;

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11 A device architecture for computing with quantum dots

Lent, C.S.; Tougaw, P.D.;

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Yariv, A.;

Selected Topics in Quantum Electronics, IEEE Journal of , Volume: 6 , Issue: 6 , Nov.-Dec. 2000
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2 **Toward an understanding of consumer experience on the Internet: implications for Website design**

Forbes, M.W.; Rothschild, M.L.;

System Sciences, 2000. Proceedings of the 33rd Annual Hawaii International Conference on , 4-7 Jan. 2000
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4 **Advanced graphics behind medical virtual reality: evolution of algorithms, hardware, and software interfaces**

Soferman, Z.; Blythe, D.; John, N.W.;

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Principe, J.C.; Ludong Wang; Motter, M.A.;

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8 Statistical response of EM-driven cables inside an overmoded enclosure

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Abdouliev, G.; Cadeddu, S.; Delussu, G.; Donizelli, M.; Formaggia, L.; Giachetti, A.; Gobbetti, E.;

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14 Substrate-lens coupled antennas for millimeter and submillimeter waves

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15 The Gas-Tightness of Separable Base Metal Electric Contacts

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16 Packaging Design Considerations Using Conventional Components

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www.bioele.nuee.nagoya-u.ac.jp/wsc1/papers/files/thompson1.ps.gz

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aspect shared by all of these systems is that they **harness** multiple FPGAs, connected in a fixed routing pins, and they use geometric properties of the **two dimensional** routing area to minimize the inter-cell or to view the FPGAs as complex entities, explicitly **modelling** both internal routing resources and pins

www.cs.washington.edu/research/lis/papers/postscript/hauck-DACpins.ps

[Active Voodoo Dolls: A Vision Based Input Device for.. - Isidoro, Sclaroff \(1998\)](#) (Correct) (2 citations)

image warping in this way, it is possible to **harness** hardware accelerated triangle texture mapping pattern. For tracking purposes, we build a **two-dimensional** deformable texture mapped **model** of a region transformations can be applied to a graphics **model**, thereby allowing the user to control motion

www.cs.bu.edu/techreports/98-006-active-voodoo-dolls.ps.Z

[ViVa: The Virtual Vascular Project - Abdoulaev, Cadeddu, Delussu..](#) (Correct)

of fast computers will make it possible to **harness** this information to provide surgeons with hand, holding a mouse, is used to operate **two-dimensional** widgets and to measure morphological vascular operations on an accurate virtual **model** of the vessel tree will allow surgeons to

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1 An Agent-Based Approach to Concurrent Cable **Harness** Design Hisup Park Lockheed Missiles and Space is discussed in the context of an industrial cable **harness** design problem. Key issues include the Insulation Jacket Ferrule Nut Cap Copper Braid **Connector** Wire Transition/assembly **Connector**/assembly cdr.stanford.edu/pub/CDR/Publications/Reports/aiedam.ps

[Connectors in Configuration Programming Languages: Are They.. - Bishop, Faria \(1996\) \(Correct\) \(11 citations\)](#)

message passing point-to-point via an occam **harness** synchronous message passing on a software -1 -**Connectors** in configuration programming languages: are in the process the properties and roles of **connectors**. By means of experiments we show how **connectors** www.cs.up.ac.za/Polelo/docs/bishop/connectors.ps.Z

[Using Pareto Optimality to Coordinate Distributed Agents - Petrie, Webster, Cutkosky \(1995\) \(Correct\) \(5 citations\)](#)

with examples from the domain of electrical cable **harness** design. Keywords: Design Coordination]Pareto scenario from our current domain: electrical cable **harness** design. It is important to carefully consider with bending and clamping constraints selecting **connectors**, shielding, and other parts and optimizing for www-cdr.stanford.edu/pub/CDR/Publications/Reports/pareto.ps.gz

[Enforcing Architecture Constraints - Robert Balzer Information \(1996\) \(Correct\) \(2 citations\)](#)

a system's architecture behavior, creates a test **harness** for exercising some portion of that system, and all communications passing through those **connectors**. Since these connections and the communications We have argued for some time that Instrumented **Connectors** were key to moving software architectures www.cs.wpi.edu/~cs562/s98/pdf/enforcing-architecture-constraints.pdf

[Electromagnetic Interference Assessment Of Cdma And Gsm.. - Jay Ely And \(2002\) \(Correct\)](#)

keypad codes, base station simulators, and a test **harness** interface. The authors are also appreciative of decrease in simulator transmit power. The test **harness** interface could control all three parameters, CDMA wireless handsets. The threat power at the **connector** of a particular aircraft radio receiver (P techreports.larc.nasa.gov/ltrs/PDF/2002/mtg/NASA-2002-21dasc-jje.pdf

[Electromagnetic Interference Assessment Of Cdmaand Gsm Wireless .. - Jay Ely And \(2002\) \(Correct\)](#)

keypad codes, base station simulators, and a test **harness** interface. The authors are also appreciative of decrease in simulator transmit power. The test **harness** interface could control all three parameters, CDMA wireless handsets. The threat power at the **connector** of a particular aircraft radio receiver (P techreports.larc.nasa.gov/pub/techreports/larc/2002/mtg/NASA-2002-21dasc-jje.ps.Z

[MAP110-1012 Power Supply MAP110-1012 Power Supply - Title Dfm Description \(Correct\)](#)

Dc Prototype Coil Driver Electronics Power **Harness** Drawn By Leandra Vicci Date Sheet Scale Dwg 15 [V]Each supply rated at 15[V]8[A]AC power **harness** mating with J1 uses Molex 09-50-3051-P shell LM 675T LM 675T RH-10 RH-10 RH-10 RH-10 Power **Connector** Power Switch DB15 DB-9 TITLE 3DFM ftp.cs.unc.edu/pub/publications/techreports/01-031.pdf

[A 20 Ampere Shunt Regulator For Controlling Individual.. - Martin Dobeck Jones \(1995\) \(Correct\)](#)

The three boards are interconnected by a wiring **harness** and plug-in **connectors**. This facilitates by use of insulated supports and a special **connector** mounted on G10 laminate. Isolation for remote Connections to a module are via a multipin **connector** which is isolated from the adjacent module and www.aps.anl.gov/conferences/mirrored/www.cern.ch/accelconf/p95/ARTICLES/RPP/RPP06.PDF

[SubjuGator: A Highly Maneuverable, Autonomous.. - Laine, Nichols.. \(2000\) \(Correct\)](#)

providers of valuable resources. In order to fully **harness** those resources, however, we must develop through the container pass through Burton **connectors** positioned on the side of the box. Bulkhead seal, and where appropriate, the holes for the **connectors** are tapped so that the **connectors** can be

www.me.ufl.edu/~dkn/papers/icra2000_sub.pdf

Computational Support for Concurrent Engineering of Cable.. - Park, Lee, Cutkosky (1992) (Correct)
Support for Concurrent Engineering of Cable **Harnesses** Hisup Park Center for Design Research,
(e.g.connectivity, geometry) of the **harnesses**. The abstraction of design information results
Insulation Jacket Ferrule Nut Cap Copper Braid **Connector** Wire Transition Assembly **Connector** &Bundle
cdr.stanford.edu/pub/CDR/Publications/Reports/FirstLink.ps

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Relevance scale

[1 Three levels of the wiring interconnection problem](#)

D. K. Frayne

January 1965

Proceedings of the SHARE design automation project

Full text available: pdf(409.79 KB)

Additional information: [full citation](#), [abstract](#), [index terms](#)

This paper describes a set of computer programs written to aid in the design, documentation, manufacture, and test of electronic hardware. The programs discussed are limited to the areas involved with the problem of wiring interconnection. While it can be said that there are over 10 different levels or types of wiring interconnection, these programs primarily concern the most common three: 1. Interconnection of circuit cards or other modules on a chassis 2. Interconnec ...

[2 Design automation and the WRAP System](#)

J. A. Brown, L. J. Cesa, J. J. Sawicki

July 1968

Proceedings of the 5th annual workshop on Design automation

Full text available: pdf(1.23 MB)

Additional information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The WRAP (Wire Routing and Packaging) System is a 7094 programming system for the automatic packaging and wiring of Aerospace computers. WRAP, originally written to assist in the packaging of IBM's System/4 Pi family of computers, has been applied to a wide range of package configurations. Use of the WRAP System has greatly reduced the time required to design and manufacture a machine. The system operates from a logic design recording system (SLDA), performs automatic packaging a ...

[3 Manufacturing engineering and information](#)

C. C. Zell

January 1967

Proceedings of the 4th conference on Design automation

Full text available: pdf(12.04 MB)

Additional information: [full citation](#), [abstract](#), [index terms](#)

This paper describes an operating computer-aided system (COMET), which processes Automated Wire List data into many forms required in manufacturing, assembly, installation and automated check out of electrical and electronic systems. The presentation presumes an operating Automated Wire List system and specifies some of the value benefits achieved at North American Aviation, Inc. The COMET description is presented from the viewpoint of Manufacturing Engineering as an interface communication ...

[4 Information, technical writing, knowledge and power: a case study of NASA's Cassini project](#)

Mattio Valentino

February 1999

ACM SIGDOC Asterisk Journal of Computer Documentation, Volume 23 Issue 1

Full text available: pdf(1.36 MB)

Additional information: [full citation](#), [abstract](#), [index terms](#)

The study of technology should not limit its scope to hardware, software, applications and systems; it should also involve language, how language shapes and is shaped by information, how information becomes knowledge, and how dominant knowledge can become power, all within the complex contexts of human culture. Technology transfer, the transfer of the knowledge of technology, is mediated by language, which, paradoxically, is itself a technology. What happens, then, when one community attempts to ...

[5 A computer aided interconnection system](#)

Richard W. Wilson

January 1969

Proceedings of the 6th annual conference on Design Automation

Full text available: pdf(380.33 KB)

Additional information: [full citation](#), [abstract](#), [index terms](#)

When electronic equipment is designed there are two ways of defining the equipment: 1) by parts content and 2) by electrical interconnections. The concern here is with electrical interconnections; how they are defined; how they flow

through the phases of design; and how a computer-aided system has helped to create an efficient design tool out of a cumbersome manual data flow. By comparing the manual system (Fig 1) with the computer-aided system (F ...

⁶ Enforcing architecture constraints

Robert Balzer

October 1996

Joint proceedings of the second international software architecture workshop (ISAW-2) and international workshop on multiple perspectives in software development (Viewpoints '96) on SIGSOFT '96 workshops

Full text available:  pdf(391.01 KB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

⁷ The design and performance of a pluggable protocols framework for real-time distributed object computing middleware

Carlos O'Ryan, Fred Kuhns, Douglas C. Schmidt, Ossama Othman, Jeff Parsons

April 2000

IFIP/ACM International Conference on Distributed systems platforms

Full text available:  pdf(231.54 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

To be an effective platform for performance-sensitive real-time and embedded applications, off-the-shelf CORBA middleware must preserve the communication-layer quality of service (QoS) properties of applications end-to-end. However, the standard CORBA GIOP/HOP interoperability protocols are not well suited for applications that cannot tolerate the message footprint size, latency, and jitter associated with general-purpose messaging and transport protocols. It is essential, therefore, to de ...

⁸ Reuse of off-the-shelf components in C2-style architectures

Nenad Medvidovic, Peyman Oreizy, Richard N. Taylor

May 1997

Proceedings of the 19th international conference on Software engineering

Full text available:  pdf(1.45 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: architectural styles, component-based development, graphical user interfaces (GUI), message-based architectures, software reuse

⁹ Reuse of off-the-shelf components in C2-style architectures

Nenad Medvidovic, Peyman Oreizy, Richard N. Taylor

May 1997

ACM SIGSOFT Software Engineering Notes , Proceedings of the 1997 symposium on Software reusability, Volume 22 Issue 3

Full text available:  pdf(1.95 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)


Keywords: architectural styles, component-based development, graphical user interfaces, message-based architectures, software reuse

¹⁰ Explaining issues in software reusability via an audio system analogy

Robert Biddle, Ewan Tempero

July 1997

Proceedings of the 2nd Australasian conference on Computer science education

Full text available:  pdf(813.16 KB)

Additional Information: [full citation](#), [references](#), [index terms](#)

¹¹ Compiler and tool support for debugging object protocols

Sergey Butkevich, Marco Renedo, Gerald Baumgartner, Michal Young

November 2000

ACM SIGSOFT Software Engineering Notes , Proceedings of the 8th ACM SIGSOFT international symposium on Foundations of software engineering: twenty-first century applications, Volume 25 Issue 6

Full text available:  pdf(1.06 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)


We describe an extension to the Java programming language that supports static conformance checking and dynamic debugging of object "protocols," i.e., sequencing constraints on the order in which methods may be called. Our Java protocols have a statically checkable subset embedded in richer descriptions that can be checked at run time. The statically checkable subtype conformance relation is based on Nierstrasz' proposal for regular (finite-state) process types, and is also very c ...

Keywords: debugging, protocols, sequencing constraints

¹² Multiple mass-market applications as components

David Coppit, Kevin J. Sullivan
June 2000

Proceedings of the 22nd international conference on Software engineering

Full text available:  pdf(179.51 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Truly successful models for component-based software development continue to prove elusive. One of the few is the use of operating system, database and similar programs in many systems. We address three related problems in this paper. First, we lack needed models. Second, we do not know the conditions under which such models can succeed. In particular, it is unclear whether the notable success with operating systems can be replicated. Third, we do not know whether certain specific models ca ...

Keywords: component-based software, package-oriented programming

13 TouchCounters: designing interactive electronic labels for physical containers

Paul Yarin, Hiroshi Ishii
May 1999

Proceedings of the SIGCHI conference on Human factors in computing systems: the CHI is the limit

Full text available:  pdf(1.42 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We present TouchCounters, an integrated system of electronic modules, physical storage containers, and shelving surfaces for the support of collaborative physical work. Through physical sensors and local displays, TouchCounters record and display usage history information upon physical storage containers, thus allowing access to this information during the performance of real-world tasks. A distributed communications network allows this data to be exchanged with a server, such that us ...

Keywords: distributed sensing, tangible interfaces, ubiquitous computing, visualization

14 A proven operational CAD system for P.W.B. design-based on a mini-computer and featuring fully automatic placement and routing.

G. L. Patterson, B. H. Phillips
June 1976

Proceedings of the 13th conference on Design automation

Full text available:  pdf(425.58 KB)


Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The introduction of electronic processor controlled switching systems has led to a re-appraisal of the methods of designing printed wiring boards to cope with the sheer numbers of designs required and their increasing complexity. The Company's policy is normally to realise the equipment on double sided printed wiring boards (pwb) containing a maximum of 80 integrated circuits per board plus discrete components. (Figs. 1 and 2) PWB requirements are frequently changing as new techn ...

15 Using the Baby-Babble-Blanket for infants with motor problems: an empirical study

H. J. Fell, H. Delta, R. Peterson, L. J. Ferrier, Z. Mooraj, M. Valleau
October 1994

Proceedings of the first annual ACM conference on Assistive technologies

Full text available:  pdf(758.52 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Children with motor problems often develop to be passive, presumably because of an inability to communicate and to control the environment. The Baby-Babble-Blanket (BBB), a pad with pressure switches linked to a Macintosh computer, was developed to meet this need. Lying on the pad, infants use head-rolling, leg-lifting and kicking to produce digitized sound. Data is collected by the BBB software on the infant's switch activations. An empirical study was carried out on a five-month-old infant ...

16 Interface to architecture: integrating technology into the environment in the Brain Opera

Maggie Orth
August 1997

Proceedings of the conference on Designing interactive systems: processes, practices, methods, and techniques

Full text available:  pdf(1.41 MB)

Additional Information: [full citation](#), [references](#), [index terms](#)

Keywords: architecture, collaboration, design, environment, furniture, interface, opera, physical interface, sensor, theater

17 Memory: part III. Reasoning and inference: The commonsense algorithm as a basis for computer models of human memory, inference, belief and contextual language comprehension

Chuck Rieger
June 1975

Proceedings of the 1975 workshop on Theoretical issues in natural language processing

Full text available:

 pdf(1.34 MB)  [Publisher Site](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

The notion of a commonsense algorithm is presented as a basic data structure for modeling human cognition. This data structure unifies many current ideas about human memory and information processing. The structure is defined by

specifying a set of proposed cognitive primitive links which, when used to build up large structures of actions, states, statechanges and tendencies, provide an adequate formalism for expressing human plans and activities, as well as general mechanisms and computer algor ...

¹⁸ Session II - empirical studies: Narratives at work: story telling as cooperative diagnostic activity

Julian E. Orr

December 1986

Proceedings of the 1986 ACM conference on Computer-supported cooperative work

Full text available:  pdf(888.15.KB)

Additional Information: [full citation](#), [abstract](#), [citations](#)

The diagnostic process for copiers involves narration of the process, including a description of the state of the machine. This follows from the fact that copiers are elaborate assemblages of relatively simple mechanisms, and the problem in diagnosis is not so much the testing of components as keeping track of the tests and making sense of their results. The anecdotal re-telling of this narrative to one's associates constitutes the mechanism for incorporating the diagnostic experience into the c ...

¹⁹ Expert systems in law: out of the research laboratory and into the marketplace

R. E. Susskind

December 1987

Proceedings of the first international conference on Artificial intelligence and law

Full text available:  pdf(1.01.MB)

Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

The major goal of workers in the field of expert systems in law can be summarized as follows: through the use of computer technology, to make scarce, human legal knowledge and expertise more widely available and easily accessible. This paper is directly concerned with that goal. It is divided into two parts. The first discusses a three year interdisciplinary research project in expert systems in law that involved the Law Faculty and the Programming Research Group of the University of Oxford ...

²⁰ A design analysis of a hybrid technology multithreaded architecture for petaflops scale computation³

Thomas Sterling, Larry Bergman

May 1999

Proceedings of the 13th international conference on Supercomputing

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Relevance scale

²¹ Maintaining the consistency of class libraries during their evolution

Mira Mezini
October 1997

ACM SIGPLAN Notices , Proceedings of the 12th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications, Volume 32 Issue 10

Full text available: pdf(3.04 MB)

Additional Information: full citation, abstract, references, citations, index terms

Two important problems of object-oriented reuse are the propagation of design and implementation specifics of the base software to the inheritors, and the protection of the inheritors against changes in the base software. In this paper, we argue that the simple inheritance rules of existing object-oriented languages are not sufficient for properly dealing with these problems. In the proposal presented in this paper, programmers are enabled to make metalevel declarations of the internal protocols ...

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